

## REMARKS/ARGUMENTS

Claims 1-13 are pending in the application; reexamination and reconsideration are hereby requested.

Claims 1, 4, 8, and 12 were objected to for various informalities; these claims have been amended.

Claims 1-7 were rejected as not enabled; the Examiner cited "signal lines" in various places conflicts with the description.

Applicant agrees and claim 1 has been amended by replacement of "signal lines" with "scanning lines" in the appropriate places.

Claims 8-9 were rejected as indefinite for lack of various antecedent bases.

Claims 8-9 have been amended to correct the antecedent bases.

Claims 1 and 5-7 were rejected as unpatentable over admitted prior art and Fujikawa; the Examiner cited application Figs.9-10 for the items of claim 1 except for the arrangement of SREG1-SREG176 with respect to drivers DR1-DR176, and added Fujikawa Fig.6 for v-drivers 25-26 corresponding to even and odd scanning rows and criss-crossed connections to the LCD panel.

Applicant replies that Fujikawa does not show the internal arrangement of the items within v-drivers 25,26 which relate to the claim 1 requirements. That is, Fujikawa shows arrangements of regions of pixels with drivers, but does not show any pertinent internal arrangements of items within the drivers. Thus the admitted prior art plus Fujikawa do not suggest independent claim 1.

Claims 8-10 and 12-13 were rejected as unpatentable over admitted prior art and Fujikawa (as per claim 1 rejection) and Wakai which shows shift register clocks  $\phi_{yo}$  and  $\phi_{ye}$ .

Applicant replies that Wakai has shift registers 401, 402 of Fig.4 with clocks  $\phi_{yo}$  and  $\phi_{ye}$  on opposite sides of the LCD panel; thus Wakai would not suggest anything about two shift registers and drivers on the same side. Consequently, the claims are patentable over the references.

An Appendix contains drawing replacement sheets with "prior art" legends added for Figs. 5-12.

Respectfully submitted,

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